

EAST Search History

EAST Search History (Prior Art)

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|--|--|------------------|---------|---------------------|
| S230 | 2884 | 348/302,303,272,308.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:48 |
| S231 | 1239 | (plural\$5 near (driv\$3 or opera \$3) near mode) | US-PGPUB; USPAT; USOCR | OR | ON | 2010/04/12 15:48 |
| S232 | 11715 | (plurality or multiple or four) near3 output near3 channels | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:49 |
| S233 | 4 | S230 and S231 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:49 |
| S234 | 28 | S230 and S232 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:49 |
| S235 | 590 | (parallel) near3 (output adj channels) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:49 |
| S236 | 1 | S231 and S234 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:50 |

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|------|-------|---|--|------|----|---------------------|
| S237 | 7 | S230 and (((read\$3out or output) with (free\$2 or unrestricted or advisedly or discretion or pleasure or candidly or deliberately or designedly or fancy-free or frankly or intentionally or openly or plainly or purposely or spontaneously or unchallenged or unreservedly or voluntarily or willingly) near3 (set or setting or decide or agree upon or allocate or allot or appoint or arrange or assign or conclude or decree or designate or determine or dictate or direct or establish or estimate or fix or fix price or impose or instruct or lay down or make or name or ordain or prescribe or price or rate or regulate or resolve or schedule or settle or specify or stipulate or value)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:50 |
| S238 | 45851 | (plurality or multiple or four) near5 ((readout or read\$4out or read or output) near3 (line or channel or mode)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:53 |
| S239 | 323 | S230 and S238 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:53 |
| S240 | 26 | S230 and S238 and S232 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/04/12 15:53 |
| S241 | 6 | S230 and (plura\$4 driv\$3 mode) and (line near3 memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | WITH | ON | 2010/04/12 15:56 |
| S242 | 32 | ((YUICHI) near2 (GOMI)). INV. | US-PGPUB; USPAT | OR | ON | 2010/04/12 15:57 |
| S243 | 28 | ((SEISUKE) near2 (MATSUDA)).INV. | US-PGPUB; USPAT | OR | ON | 2010/04/12 15:57 |
| S244 | 18 | ((YUKIHIRO) near2 (KURODA)).INV. | US-PGPUB; USPAT | OR | ON | 2010/04/12 15:57 |
| S245 | 86 | ((KEIICHI) near2 (MORI)). INV. | US-PGPUB; USPAT | OR | ON | 2010/04/12 15:57 |

EAST Search History (Interference)

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|----------------|------------------|---------|------------------|
| S246 | 0 | (line memories which are arranged between the pixels and the output channels and which temporarily store pixel signals of the pixels selected and read out in the first driving mode and the second driving mode).clm. | US-PGPUB; UPAD | WITH | ON | 2010/04/12 15:58 |
| S247 | 0 | (a control circuit which is arranged between the pixels and the line memories and which sets a driving mode to one of the first driving mode and the second driving mode based on an externally input signal which may be freely set).clm. | US-PGPUB; UPAD | WITH | ON | 2010/04/12 15:58 |
| S248 | 0 | (the number of output channels to which the pixel signals are output in the first driving mode and the number of output channels to which the pixel signals are output in the second driving mode are different).clm. | US-PGPUB; UPAD | WITH | ON | 2010/04/12 15:58 |
| S249 | 0 | (line memories arranged pixels output channels temporarily store pixel signals pixels selected read out first driving mode second driving mode control circuit arranged pixels line memories sets driving mode first driving mode second based externally input signal freely set number output channels pixel signals output first driving mode number output channels pixel signals output second driving mode different).clm. | US-PGPUB; UPAD | WITH | ON | 2010/04/12 15:59 |

4/12/2010 4:00:52 PM**C:\Documents and Settings\kwang\My Documents\EAST\Workspaces\10808688.wsp**